



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2818\$
IFW

Leonard Forbes et al.

Title: PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND
ASYMMETRICAL TUNNEL BARRIERS

Docket No.: 1303.035US1

Serial No.: 10/028,001

Filed: December 20, 2001

Due Date: January 12, 2006

Examiner: Tu-Tu V. Ho

Group Art Unit: 2818

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Amendment and Response (15 pgs.).
- ☒ Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.).
- ☒ Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).
- ☒ Communication Concerning Related Applications (1 pg.).

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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By: Marvin L. Beekman
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12 day of January, 2006.

Lisa Posorske
Name

Lisa Posorske
Signature

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(GENERAL)



SN 10/028,001

PATENT

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Applicant: Leonard Forbes et al. Examiner: Tu-Tu V. Ho
Serial No.: 10/028,001 Group Art Unit: 2818
Filed: December 20, 2001 Docket: 1303.035US1
Title: PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL
DEVICES AND ASYMMETRICAL TUNNEL BARRIERS

COMMUNICATION CONCERNING RELATED APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11/202,460	August 12, 2005	1303.027US3	SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

LEONARD FORBES ET AL.

By Applicants' Representatives,

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